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175 11/6/03

October 30, 2003

**MAIL STOP PATENT APPLICATION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: Application of Mohamed Shaker SARWARY, Mohammad Movahed EZAZI and  
Bernard MURPHY  
METHOD FOR CLOCK SYNCHRONIZATION VALIDATION IN  
INTEGRATED CIRCUIT DESIGN  
**Assignee: ATRENTA INC.**  
Our Ref. Q77571

Dear Sir:

Attached hereto is the application identified above comprising twenty-eight (28) sheets of the specification, including the claims and abstract, five (5) sheets of drawings, a copy of an executed Assignment and PTO 1595 form, and a copy of an executed Declaration and Power of Attorney.

The Government filing fee is calculated as follows:

Total claims	<u>45</u>	-	<u>20</u>	=	<u>25</u>	x	\$18.00	=	<u>\$450.00</u>
Independent claims	<u>3</u>	-	<u>3</u>	=		x	\$86.00	=	<u>\$0.00</u>
Base Fee									<u>\$770.00</u>

<b>TOTAL FILING FEE</b>	<b>\$1220.00</b>
Recordation of Assignment	\$40.00
<b>TOTAL FEE</b>	<b><u>\$1260.00</u></b>

Checks for the statutory filing fee of **\$1220.00** and Assignment recordation fee of **\$40.00** are attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this transmittal letter is attached.

Respectfully submitted,  
SUGHRU MION, PLLC

Attorneys for Applicant  
By: Kelly G. Hyndman  
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